

FIGURE 1  
PRIOR ART

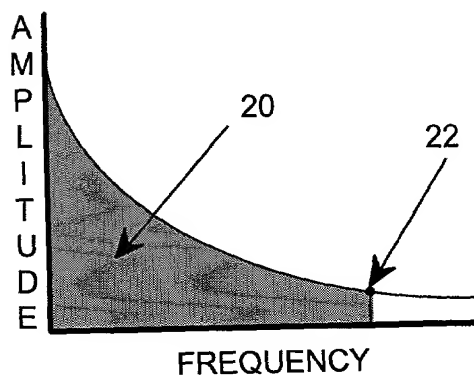
[illegible]

Figure 3: Frequency Detector Signal Flow Block Diagram

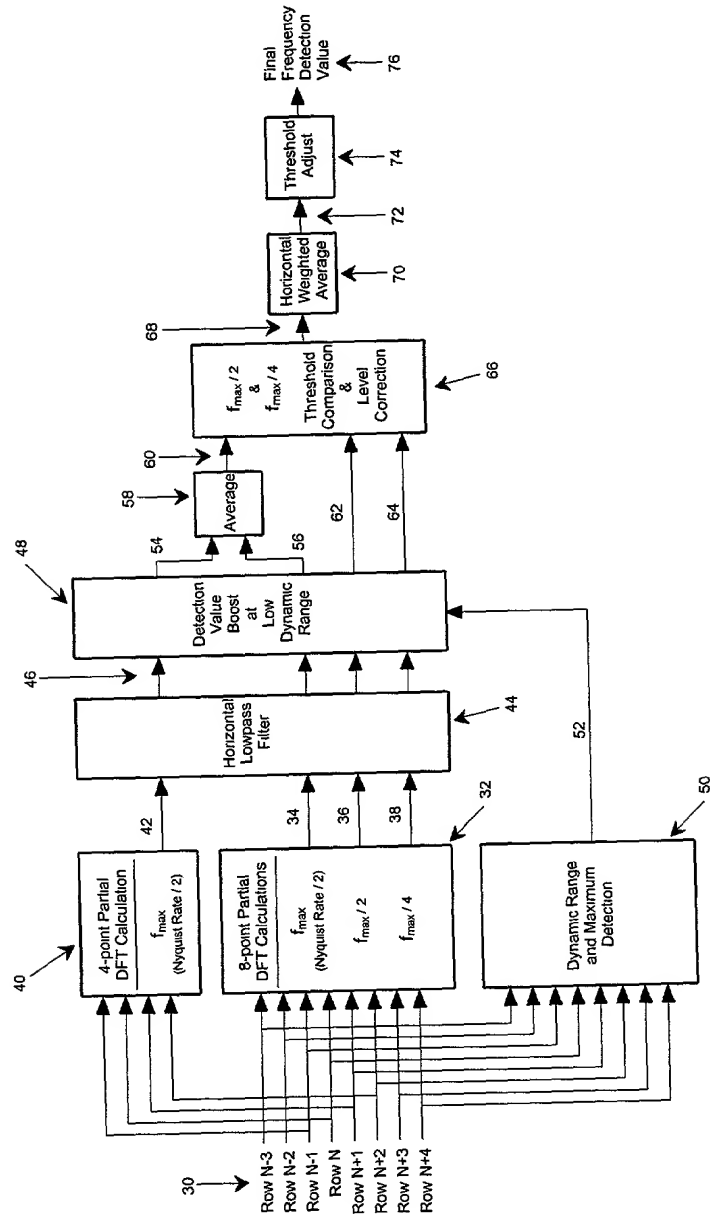


Figure 4: Threshold Adjustment Detail

